

# DB2-xxDxxD Series

DC-DC Converter | 2W | DIP14 | Fixed voltage input, unregulated output | 1500VDC



## Features

- Small DIP14 Package
- Operating Temperature Range: -40°C ~ +105°C
- Isolation Voltage: 1500VDC
- Full Load Efficiency: up to 90% (typ.)
- Continuous Short Circuit Protection
- Designed to meet IEC/EN/BS EN/UL 62368

## Product Description



The DB2-xxDxxD series is 2W unregulated DC/DC converters that are typically used in cost sensitive general purpose power isolation and voltage matching applications. Typical applications are digital interfaces, voltage conversion in distributed powersy stems, general low-frequency analog circuits, relay drive circuits, data switching circuits etc.

## Selection Guide

Certification	Part No.	Input Voltage		Output		Full Load Efficiency (%)	Capacitive Load (μF)
		Nominal (Range) (VDC)	Voltage (VDC)	Current (mA) Min.	Current (mA) Max.		
EN/UL Pending	DB2-03D03D	3.3 (2.97~3.63)	±3.3	20	±200	77	1000
	DB2-03D05D	3.3 (2.97~3.63)	±5	20	±200	79	1000
	DB2-03D09D	3.3 (2.97~3.63)	±9	11	±111	80	470
	DB2-03D12D	3.3 (2.97~3.63)	±12	8	±83	81	220
	DB2-03D15D	3.3 (2.97~3.63)	±15	6	±67	82	220
	DB2-03D24D	3.3 (2.97~3.63)	±24	4	±42	82	100
	DB2-05D03D	5 (4.5~5.5)	±3.3	20	±200	82	1000
	DB2-05D05D	5 (4.5~5.5)	±5	20	±200	86	1000
	DB2-05D09D	5 (4.5~5.5)	±9	11	±111	86	470
	DB2-05D12D	5 (4.5~5.5)	±12	8	±83	86	220
	DB2-05D15D	5 (4.5~5.5)	±15	6	±67	87	220
	DB2-05D24D	5 (4.5~5.5)	±24	4	±42	88	100
	DB2-12D03D	12 (10.8~13.2)	±3.3	20	±200	82	1000
	DB2-12D05D	12 (10.8~13.2)	±5	20	±200	86	1000

## Selection Guide

Certification	Part No.	Input Voltage	Output			Full Load Efficiency (%) Typ.	Capacitive Load ( $\mu$ F) Max.*
		Nominal (Range) (VDC)	Voltage (VDC)	Current (mA) Min.	Current (mA) Max.		
EN/UL pending	DB2-12D09D	12 (10.8~13.2)	$\pm$ 9	11	$\pm$ 111	88	470
	DB2-12D12D	12 (10.8~13.2)	$\pm$ 12	8	$\pm$ 83	88	220
	DB2-12D15D	12 (10.8~13.2)	$\pm$ 15	6	$\pm$ 67	89	220
	DB2-12D24D	12 (10.8~13.2)	$\pm$ 24	4	$\pm$ 42	90	100
	DB2-15D03D	15 (13.5~16.5)	$\pm$ 3.3	20	$\pm$ 200	83	1000
	DB2-15D05D	15 (13.5~16.5)	$\pm$ 5	20	$\pm$ 200	85	1000
	DB2-15D09D	15 (13.5~16.5)	$\pm$ 9	11	$\pm$ 111	86	470
	DB2-15D12D	15 (13.5~16.5)	$\pm$ 12	8	$\pm$ 83	87	220
	DB2-15D15D	15 (13.5~16.5)	$\pm$ 15	6	$\pm$ 67	90	220
	DB2-15D24D	15 (13.5~16.5)	$\pm$ 24	4	$\pm$ 42	89	100
	DB2-24D03D	24 (21.6~26.4)	$\pm$ 3.3	20	$\pm$ 200	85	1000
	DB2-24D05D	24 (21.6~26.4)	$\pm$ 5	20	$\pm$ 200	86	1000
	DB2-24D09D	24 (21.6~26.4)	$\pm$ 9	11	$\pm$ 111	87	470
	DB2-24D12D	24 (21.6~26.4)	$\pm$ 12	8	$\pm$ 83	89	220
	DB2-24D15D	24 (21.6~26.4)	$\pm$ 15	6	$\pm$ 67	90	20
	DB2-24D24D	24 (21.6~26.4)	$\pm$ 24	4	$\pm$ 42	90	100

Note:

- \*The capacitive load of the two outputs for positive and negative output is the same.
- The above efficiency is measured at nominal input voltage and rated output load.

## Input Specifications

Item	Operating Conditions		Min.	Typ.	Max.	Unit
Input Current (Full Load/No-load)	3.3VDC Input	3.3VDC Output	--	787/10	808/15	mA
		5VDC Output	--	766/10	787/15	mA
	5VDC Input	3.3VDC Output	--	488/8	500/15	mA
		5VDC Output	--	465/8	476/15	mA
		9/12VDC Output	--	465/10	476/15	mA
		15/24VDC Output	--	455/25	465/35	mA
	12VDC Input	3.3VDC Output	--	203/7	208/15	mA
		5VDC Output	--	194/7	198/15	mA
		Others	--	189/8	194/15	mA
	15VDC Input	3.3VDC Output	--	161/6	165/15	mA
		5/9/12VDC Output	--	157/6	161/15	mA
		Others	--	150/6	153/15	mA
	24VDC Input	3.3VDC Output	--	98/4	100/15	mA
		5/9VDC Output	--	97/4	99/15	mA
		Others	--	94/5	96/15	mA
Reflected Ripple Current			--	15	--	mA
Surge Voltage	3.3VDC Input		-0.7	--	5	VDC
	5VDC Input		-0.7	--	9	VDC
	12VDC Input		-0.7	--	18	VDC
	15VDC Input		-0.7	--	21	VDC
	24VDC Input		-0.7	--	30	VDC
Input Filter Type			Capacitance Filter			
Hot Plug			Unavailable			

## Output Specifications

Item	Operating Conditions		Min.	Typ.	Max.	Unit
Output Voltage Accuracy			See Output Regulation Curve			
Line Regulation	Input Voltage Variation: $\pm 1\%$	3.3V Output	--	$\pm 1.5$	--	%
		Others	--	$\pm 1.2$	--	%
Load regulation	10%~100% Load	3.3VDC Output	--	12	--	%
		5VDC Output	--	12	--	%
		9VDC Output	--	8	--	%
		12VDC Output	--	8	--	%
		15VDC Output	--	8	--	%
		24VDC Output	--	6	--	%
Ripple & noise	20MHz bandwidth (peak-to-peak)		--	60	120	mV

Temperature coefficient	Full load	--	±0.02	--	%/°C
Short circuit protection		Continuous, self-recovery			

Note: Ripple & noise are measured at 20MHz of bandwidth with a 10uF electrolytic capacitor and a 1uF ceramic capacitor connected inparallel at the output.

## General Specifications

Item	Operating Conditions	Min.	Typ.	Max.	Unit
Isolation Voltage	Input-output, test time 1 minute, leakage current less than 1mA	1500	--	--	VDC
Insulation Resistance	Input-output, insulated voltage 500VDC	1000	--	--	MΩ
Isolation Capacitance	Input-Output, 100KHz/0.1V	--	20	--	pF
Operating Temperature	Derating when operating temperature ≥ 85°C (See Temperature derating curve chart)	-40	--	105	°C
Storage Temperature		-55	--	125	°C
Case Temperature Rise	Ta=25°C, Nominal Input, Full Load	--	25	--	°C
Storage Humidity	Non-condensing	5	--	95	%RH
Soldering Profile	Wave soldering	260±5°C; Time: 5 - 10 seconds			
	Manual soldering	360±10°C; Time: 3 - 5 seconds			
Switching Frequency	Full Load, Nominal Input Voltage	--	220	--	kHz
MTBF	MIL-HDBK-217F@25°C	>3500Kh			

## Mechanical Specifications

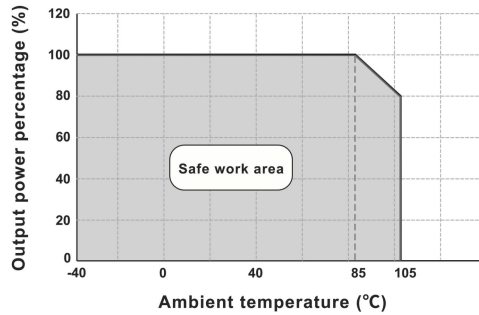
Case Material	Black flame-retardant heat-resistant plastic (UL94V-0)
Mechanical Dimensions	19.50 * 9.80 * 7.00mm
Weight	2.0g(typ.)
Cooling Method	Free air convection

## Electromagnetic Compatibility (EMC)

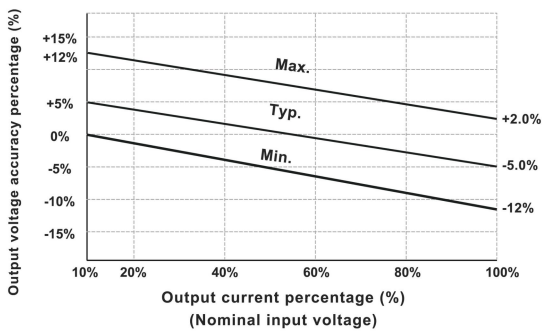
EMI	CE	CISPR32/EN55032 CLASS B (EMC recommended circuit see Fig.2)
	RE	CISPR32/EN55032 CLASS B (EMC recommended circuit see Fig.2)
EMS	ESD	IEC/EN61000-4-2 Contact±6KV Air ±8KV
		perf. Criteria B

Product Characteristic Curve

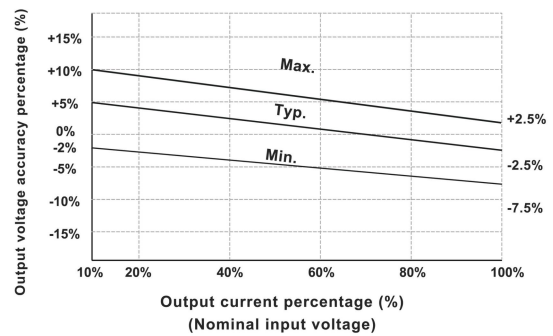
Temperature Derating Curve



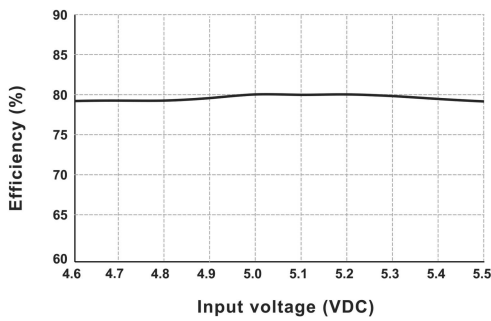
Output Regulation Curve (3.3V Output)



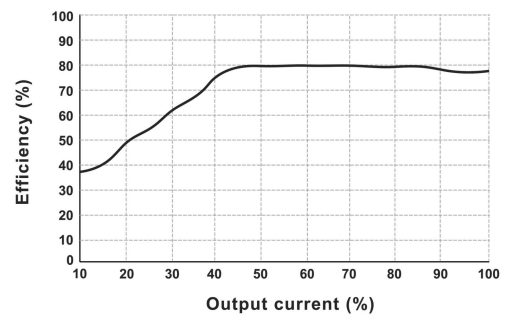
Output Regulation Curve (Others)



Efficiency vs. Input Voltage (Full Load, DB2-05D05D)

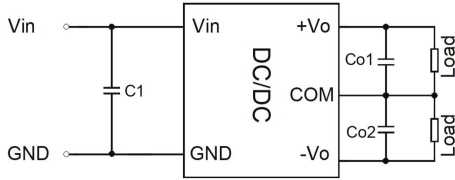


Efficiency vs. Output Load (Vin=5V, DB2-05D05D)



## Design Reference - Application circuit

Application circuit



(Figure 1)

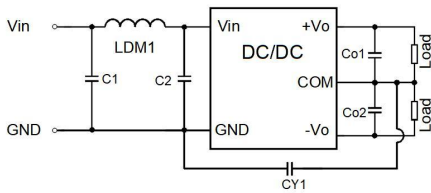
Recommended Capacitive Load Value Table

Vin	C1	Vo	Co1/Co2
3.3/5VDC	4.7uF/16V	3.3/5VDC	10μF/16V
12VDC	2.2uF/25V	9/12VDC	4.7μF/25V
15VDC	1.0uF/25V	15VDC	1.0μF/25V
24VDC	1.0uF/50V	24VDC	0.47μF/50V

All DC/DC converters in this series are tested according to the recommended application circuit (Figure 1) before the shipment. If further reduction of input and output ripple is required, the external capacitors  $C_{in}/C_{out}$  at the input and output can be increased, or capacitors with lower equivalent series impedance can be selected. For each output, under safe and reliable operating conditions, the maximum capacitance of the filter capacitor must not exceed the maximum capacitive load of the product.

## Design Reference - EMC Solutions - Recommended Circuits

EMC Recommended Circuit Design and Application



(Figure 2)

Recommended parameter values

C1	4.7μF /50V
C2	4.7μF /50V
Co1/Co2	Refer to Cout parameter in Figure 1
CY1	1000pF/3kV
LDM1	6.8μH

Note:

### 1. Typical Application

If further reduction of input and output ripple is required, a Capacitance Filter network can be connected at the input and output terminals, with the application circuit shown in Figure 1. However, appropriate filter capacitors should be selected. If the capacitor is too large, it may cause startup issues. For each output, under conditions ensuring safe and reliable operation, the recommended capacitive load values are detailed in the table.

### 2. EMC Recommended Circuit: See Figure 2

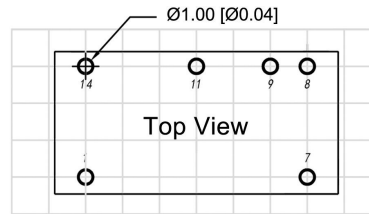
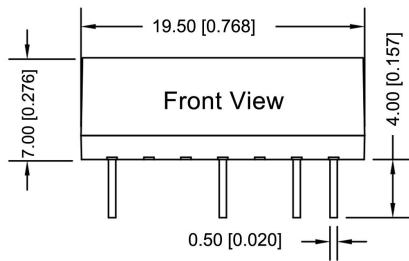
### 3. Output Load Requirements

To ensure efficient and reliable operation of the module, the minimum output load during use must not be less than 10% of the rated load. If the required power is indeed small, please connect a resistor in parallel at the output (the sum of the power dissipated by the resistor and the actual power used must be greater than or equal to 10% of the rated power).

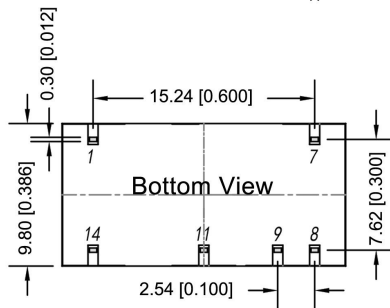
## Dimensions and Recommended Layout

### DB2-xxDxxD Dimensions and Recommended Layout

Third Angle Projection



The grid distance size is 2.54mm\*2.54mm



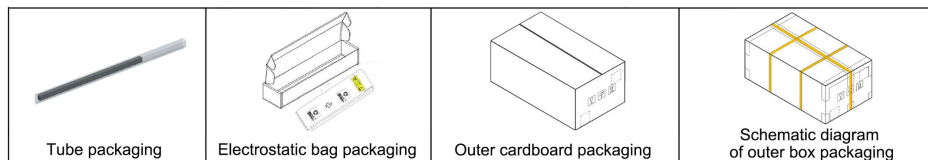
Note:  
 Size unit: mm [inch]  
 Pin diameter tolerance:  $\pm 0.10$  [ $\pm 0.004$ ]  
 Unmarked dimensional tolerance:  $\pm 0.50$  [ $\pm 0.020$ ]

Pin	Function (Dual Output)
1	GND
7	NC
8	COM
9	+Vo
11	-Vo
14	Vin

## Packaging Information

Model series (Tube packaging)	Quantity per tube (pcs/ tube)	Quantity of electrostatic bag (pcs/ bag)	Quantity of inner box (pcs/ box)	Full box Quantity (pcs)
DB2-xxDxxD	25	250	1000	4000

The schematic diagram of tube packaging is shown below:



## Product precautions

1. The input voltage should not exceed the specified range value, otherwise it may cause permanent and irreparable damage;
2. It is recommended to use at a load of over 5%. If the load is below 5%, the ripple index of the product may exceed the specifications, but it does not affect the reliability of the product;
3. The maximum capacitive load is tested within the input voltage range and under full load conditions;
4. Unless otherwise specified, all indicators in this manual are measured at  $T_a=25\text{ }^\circ\text{C}$ , humidity<75% RH, nominal input voltage, and output rated load;
5. All indicator testing methods in this manual are based on our company's corporate standards;
6. Our company can provide product customization, and specific requirements can be directly contacted by our technical personnel;
7. Product specifications are subject to change without prior notice.

## Manufacturer contact information

### Bettpower Guangzhou Electronic Technology Co., Ltd.

Website: [www.bettpower.com](http://www.bettpower.com)

Telephone: +86 - 020 - 32166256

Email: [info@bettpower.com](mailto:info@bettpower.com)

Address: Room 2514-2515, Building A1,1 Doutang Road, Huangpu District, Guangzhou, China

BETTPOWER is a registered trademark of BETTPOWER Guangzhou Electronic Technology Co., Ltd. All of its product names, models, trademarks and brands are the property of the Company.

BETTPOWER Guangzhou Electronic Technology Co., Ltd reserves all rights and the right of final interpretation.