2025/08/18 (A/2)

DB2-xxSxxD Series

DC-DC Converter | 2W | DIP14 | Fixed voltage input, unregulated output | 1500VDC

Features

- Small DIP14 Package
- Operating Temperature Range: -40℃ ~ +105℃
- Isolation Voltage: 1500VDC
- Full Load Efficiency: up to 90% (typ.)
- Continuous Short Circuit Protection
- Designed to meet IEC/EN/BS EN/UL 62368

Product Description

Industrial Automate Power Security

The DB2-xxSxxD series is 2W unregulated DC/DC converters that are typically used in cost sensitive general purpose power isolation and voltage matching applications. Typical applications are digital interfaces, voltage conversion in distributed powersy stems, general low-frequency analog circuits, relay drive circuits, data switching circuits etc.

Selection Guide

Certification	Part No.	Input Voltage	Output			Full Load Efficiency (%)	Capacitive Load (μF)
		Nominal (Range) (VDC)	Voltage (VDC)	Current (mA) Min.	Current (mA) Max.	Typ.	Max.
EN/UL Pending	DB2-03S03D	3.3 (2.97~3.63)	3.3	40	400	77	2400
	DB2-03S05D	3.3 (2.97~3.63)	5	40	400	79	2400
	DB2-03S09D	3.3 (2.97~3.63)	9	22	222	80	1000
	DB2-03S12D	3.3 (2.97~3.63)	12	17	167	81	560
	DB2-03S15D	3.3 (2.97~3.63)	15	13	133	82	560
	DB2-03S24D	3.3 (2.97~3.63)	24	9	83	82	220
	DB2-05S03D	5 (4.5~5.5)	3.3	40	400	82	2400
	DB2-05S05D	5 (4.5~5.5)	5	40	400	86	2400
	DB2-05S09D	5 (4.5~5.5)	9	22	222	86	1000
	DB2-05S12D	5 (4.5~5.5)	12	17	167	86	560
	DB2-05S15D	5 (4.5~5.5)	15	13	133	87	560
	DB2-05S24D	5 (4.5~5.5)	24	9	83	88	220
	DB2-12S03D	12 (10.8~13.2)	3.3	40	400	82	2400
	DB2-12S05D	12 (10.8~13.2)	5	40	400	86	2400

Selection Guide

Certification	Part No.	Input Voltage	Output			Full Load	Capacitive
		Nominal (Range) (VDC)	Voltage (VDC)	Current (mA) Min.	Current (mA) Max.	Efficiency (%) Typ.	Load (μF) Max.
EN/UL Pending	DB2-12S09D	12 (10.8~13.2)	9	22	222	88	1000
	DB2-12S12D	12 (10.8~13.2)	12	17	167	88	560
	DB2-12S15D	12 (10.8~13.2)	15	13	133	89	560
	DB2-12S24D	12 (10.8~13.2)	24	9	83	90	220
	DB2-15S03D	15 (13.5~16.5)	3.3	40	400	83	2400
	DB2-15S05D	15 (13.5~16.5)	5	40	400	85	2400
	DB2-15S09D	15 (13.5~16.5)	9	22	222	86	1000
	DB2-15S12D	15 (13.5~16.5)	12	17	167	87	560
	DB2-15S15D	15 (13.5~16.5)	15	13	133	90	560
	DB2-15S24D	15 (13.5~16.5)	24	9	83	89	220
	DB2-24S03D	24 (21.6~26.4)	3.3	40	400	85	2400
	DB2-24S05D	24 (21.6~26.4)	5	40	400	86	2400
	DB2-24S09D	24 (21.6~26.4)	9	22	222	87	1000
	DB2-24S12D	24 (21.6~26.4)	12	17	167	92	560
	DB2-24S15D	24 (21.6~26.4)	15	13	133	90	560
	DB2-24S24D	24 (21.6~26.4)	24	9	83	90	220

Note: The above efficiency is measured at nominal input voltage and rated output load.

Input Specifications

Item	Operating Conditions		Min.	Typ.	Max.	Unit
Input Current (Full Load/No-load)	3.3VDC Input	3.3VDC Output	--	520/10	533/15	mA
		5/9VDC Output	--	756/10	767/15	mA
		Others	--	736/12	760/18	mA
	5VDC Input	3.3VDC Output	--	321/8	330/15	mA
		5/9/12VDC Output	--	465/8	475/15	mA
		15/24VDC Output	--	450/10	463/15	mA
	12VDC Input	3.3VDC Output	--	134/7	140/15	mA
		5VDC Output	--	193/7	193/15	mA
		Others	--	184/8	194/15	mA
	15VDC Input	3.3VDC Output	--	106/6	110/15	mA
		5/9/12VDC Output	--	153/6	160/15	mA
		Others	--	147/6	152/15	mA
	24VDC Input	3.3VDC Output	--	64/3	70/15	mA
		5/9VDC Output	--	95/3	100/15	mA
		Others	--	48/4	50/15	mA
Reflected Ripple Current			--	15	--	mA
Surge Voltage	3.3VDC Input		-0.7	--	9	VDC
	5VDC Input		-0.7	--	9	VDC
	12VDC Input		-0.7	--	18	VDC
	15VDC Input		-0.7	--	21	VDC
	24VDC Input		-0.7	--	30	VDC
Input Filter Type			Capacitance Filter			
Hot Plug			Unavailable			

Output Specifications

Item	Operating Conditions		Min.	Typ.	Max.	Unit
Output Voltage Accuracy			See Output Regulation Curve			
Line Regulation	Input Voltage Variation: ±1%	3.3V Output	--	±1.5	--	%
		Others	--	±1.2	--	%
Load regulation	10%~100% load	3.3VDC Output	--	10	--	%
		5VDC Output	--	10	--	%
		9VDC Output	--	8	--	%
		12VDC Output	--	8	--	%
		15VDC Output	--	8	--	%
		24VDC Output	--	6	--	%
Ripple & noise	20MHz bandwidth, 100% load		--	60	120	mV

Temperature coefficient	Full load	--	±0.02	--	%/℃
Short circuit protection		Continuous, self-recovery			

Note: Ripple & noise are measured at 20MHz of bandwidth with a 10uF electrolytic capacitor and a 1uF ceramic capacitor connected inparallel at the output.

General Specifications

Item	Operating Conditions	Min.	Typ.	Max.	Unit
Isolation Voltage	Input-output, test time 1 minute, leakage current less than 1mA	1500	--	--	VDC
Insulation Resistance	Input-output, insulated voltage 500VDC	1000	--	--	MΩ
Isolation Capacitance	Input-Output,100KHz/0.1V	--	20	--	pF
Operating Temperature	Derating when operating temperature≥85℃ (See Temperature derating curve chart)	-40	--	105	℃
Storage Temperature		-55	--	125	℃
Case Temperature Rise	Ta=25℃, Nominal Input, Full Load	--	25	--	℃
Storage Humidity	Non-condensing	5	--	95	%RH
Soldering Profile	Wave soldering	260±5℃; Time: 5 - 10 seconds			
	Manual soldering	360±10℃; Time: 3 - 5 seconds			
Switching Frequency	Full load, nominal input voltage	--	220	--	kHz
MTBF	MIL-HDBK-217F@25℃	>3500Kh			

Mechanical Specifications

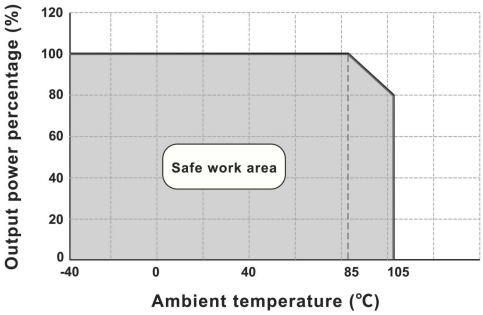
Case Material	Black flame-retardant heat-resistant plastic (UL94V-0)
Mechanical Dimensions	19.50 * 9.80 * 7.00mm
Weight	2.35g(typ.)
Cooling Method	Free air convection

Electromagnetic Compatibility (EMC)

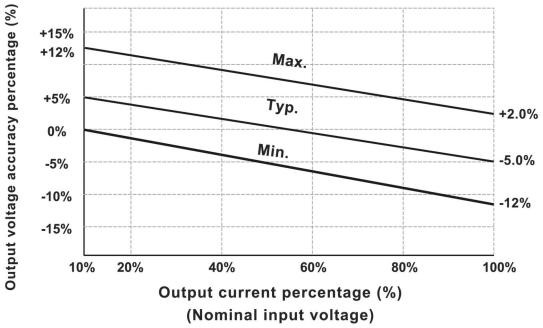
EMI	CE	CISPR32/EN55032 CLASS B (EMC recommended circuit see Fig. 2)	
	RE	CISPR32/EN55032 CLASS B (EMC recommended circuit see Fig. 2)	
EMS	ESD	IEC/EN61000-4-2 Contact ±4KV Air ±8KV	perf. Criteria B

Product Characteristic Curve

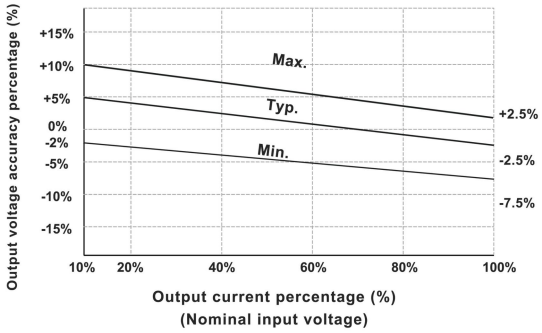
Temperature Derating Curve



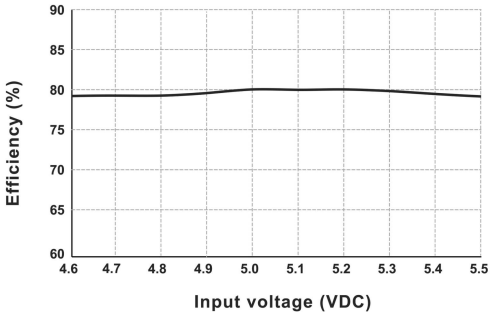
Output Regulation Curve(3.3V Output)



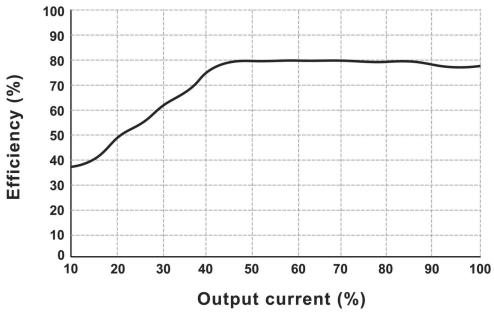
Output Regulation Curve (Others)



Efficiency VS Input Voltage (Full Load, DB2-05S05D)

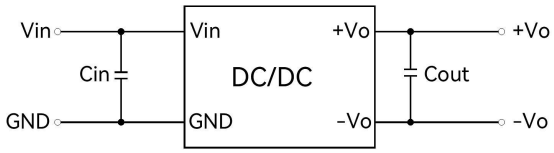


Efficiency VS Output Load (Vin=5V, DB2-05S05D)



Design Reference - Application Circuit

Application circuit



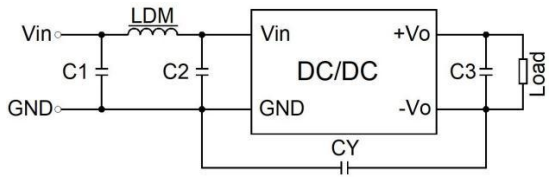
(Figure 1)

Recommended Capacitive Load Value Table			
Vin	Cin	Vo	Cout
3.3/5VDC	4.7uF/16V	3.3/5VDC	10μF/16V
12VDC	2.2uF/25V	9/12VDC	4.7μF/25V
15VDC	2.2uF/50V	15VDC	1.0μF/50V
24VDC	1.0uF/50V	24VDC	0.47μF/50V

All DC/DC converters in this series are tested according to the recommended application circuit (Figure 1) before the shipment. If further reduction of input and output ripple is required, the external input and output capacitors Cin/Cout can be increased, or capacitors with lower series equivalent impedance can be selected. For each output, under safe and reliable operating conditions, the maximum capacitance of the filter capacitor must not exceed the maximum capacitive load of the product.

Design Reference - EMC Solutions - Recommended Circuits

EMC Recommended Circuit Design and Application



(Figure 2)

Recommended parameter values	
C1	4.7μF /50V
C2	4.7μF /50V
C3	Refer to the Cout parameter in Figure 1
CY	1000pF/3kV
LDM	6.8μH

Note:

1. Typical Application

To further reduce input and output ripple, a Capacitance Filter network can be connected at the input and output terminals. The application circuit is shown in Figure 1. However, appropriate filter capacitors should be selected. If the capacitor is too large, it may cause startup issues. For each output, under conditions ensuring safe and reliable operation, the recommended capacitive load values are detailed in the table.

2. EMC Recommended Circuit: See Figure 2

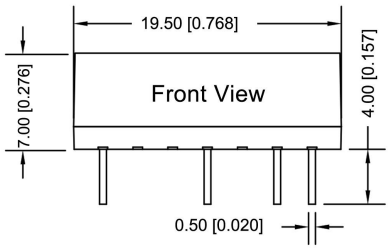
3. Output Load Requirements

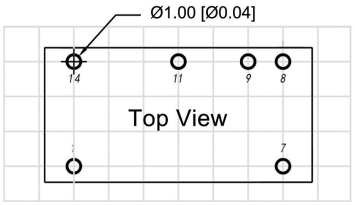
To ensure efficient and reliable operation of the module, the minimum output load during use must not be less than 10% of the rated load. If the required power is indeed small, please connect a resistor in parallel at the output terminal (the sum of the resistor's power dissipation and the actual used power must be greater than or equal to 10% of the rated power.

Dimensions and Recommended Layout

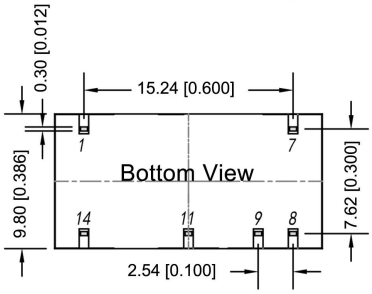
DB2-xxSxxD Dimensions and Recommended Layout

Third Angle Projection





The grid distance size is 2.54mm*2.54mm



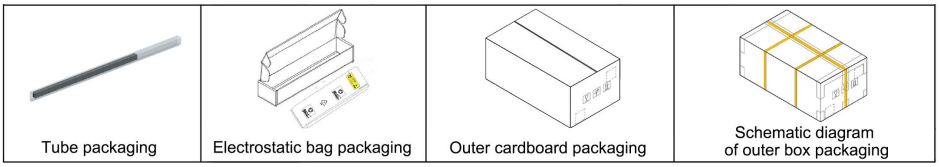
Pin	Function (Single Output)
1	GND
7	NC
8	-Vo
9	+Vo
11	No Pin
14	Vin

Note:
Size unit: mm [inch]
Pin diameter tolerance: ±0.10 [±0.004]
Unmarked dimensional tolerance: ±0.50 [±0.020]

Packaging Information

Model series (Tube packaging)	Quantity per tube (pcs/ tube)	Quantity of electrostatic bag (pcs/ bag)	Quantity of inner box (pcs/ box)	Full box Quantity (pcs)
DB2-xxSxxD	25	250	1000	4000

The schematic diagram of tube packaging is shown below:



Product precautions

1. The input voltage should not exceed the specified range value, otherwise it may cause permanent and irreparable damage;
2. It is recommended to use at a load of over 5%. If the load is below 5%, the ripple index of the product may exceed the specifications, but it does not affect the reliability of the product;
3. The maximum capacitive load is tested within the input voltage range and under full load conditions;
4. Unless otherwise specified, all indicators in this manual are measured at Ta=25 ℃, humidity<75% RH, nominal input voltage, and output rated load;
5. All indicator testing methods in this manual are based on our company's corporate standards;
6. Our company can provide product customization, and specific requirements can be directly contacted by our technical personnel;
7. Product specifications are subject to change without prior notice.

Manufacturer contact information

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