

DF2-xxSxxLT Series

DC-DC Converter | 2W | SMD12 | Fixed voltage input, unregulated output | 3000VDC



Features

- SMD12 Package
- Operating Temperature Range: -40°C ~ +105°C
- Isolation Voltage: 3000VDC
- Full Load Efficiency: 88% (typ.)
- Continuous Short Circuit Protection
- Designed to meet IEC/EN/BS EN/UL 62368

Product Description



The DF2-xxSxxLT series is 2W unregulated DC/DC converters that are an ideal and economical solutions for on-board power applications where an isolated voltage is required. Typical applications are digital interfaces, voltage conversion in distributed power systems, general low-frequency analog circuits, relay drive circuits, data exchange circuits, etc. For automated SMD production lines, this series can be supplied in standard tape and reel package.

Selection Guide

Certification	Part No.	Input Voltage	Output			Full Load Efficiency (%) Typ.	Capacitive Load (μF) Max.*
		Nominal (Range) (VDC)	Voltage (VDC)	Current (mA) Min.	Current (mA) Max.		
EN/UL Pending	DF2-05S03LT	5 (4.5~5.5)	3.3	40	400	81	2400
	DF2-05S05LT	5 (4.5~5.5)	5	40	400	83	2400
	DF2-05S09LT	5 (4.5~5.5)	9	22	222	85	1000
	DF2-05S12LT	5 (4.5~5.5)	12	16	167	85	560
	DF2-05S15LT	5 (4.5~5.5)	15	13	133	85	560
	DF2-05S24LT	5 (4.5~5.5)	24	8	83	85	220
	DF2-12S03LT	12 (10.8~13.2)	3.3	40	400	82	2400
	DF2-12S05LT	12 (10.8~13.2)	5	40	400	83	2400
	DF2-12S09LT	12 (10.8~13.2)	9	22	222	84	1000
	DF2-12S12LT	12 (10.8~13.2)	12	16	167	87	560
	DF2-12S15LT	12 (10.8~13.2)	15	13	133	87	560
	DF2-12S24LT	12 (10.8~13.2)	24	8	83	87	220
	DF2-15S03LT	15 (13.5~16.5)	3.3	40	400	82	2400

Selection Guide

Certification	Part No.	Input Voltage	Output			Full Load Efficiency (%) Typ.	Capacitive Load (μF) Max.*
		Nominal (Range) (VDC)	Voltage (VDC)	Current (mA) Min.	Current (mA) Max.		
EN/UL Pending	DF2-15S05LT	15 (13.5~16.5)	5	40	400	83	2400
	DF2-15S09LT	15 (13.5~16.5)	9	22	222	85	1000
	DF2-15S12LT	15 (13.5~16.5)	12	16	167	87	560
	DF2-15S15LT	15 (13.5~16.5)	15	13	133	88	560
	DF2-15S24LT	15 (13.5~16.5)	24	8	83	88	220
	DF2-24S03LT	24 (21.6~26.4)	3.3	40	400	82	2400
	DF2-24S05LT	24 (21.6~26.4)	5	40	400	84	2400
	DF2-24S09LT	24 (21.6~26.4)	9	22	222	85	1000
	DF2-24S12LT	24 (21.6~26.4)	12	16	167	88	560
	DF2-24S15LT	24 (21.6~26.4)	15	13	133	88	560
	DF2-24S24LT	24 (21.6~26.4)	24	8	83	88	220

Note: The above efficiency is measured at nominal input voltage and rated output load.

Input Specifications

Item	Operating Conditions		Min.	Typ.	Max.	Unit
Input Current (Full Load/No-load)	5VDC input	3.3VDC Output	--	326/8	334/15	mA
		5VDC Output	--	482/8	494/15	mA
		9/12VDC Output	--	470/10	482/15	mA
		15/24VDC output	--	470/25	482/35	mA
	12VDC input	3.3VDC output	--	134/8	138/15	mA
		5/9VDC output	--	200/8	205/15	mA
		Others	--	192/8	197/15	mA
	15VDC input	3.3VDC output	--	107/7	110/15	mA
		5/9VDC output	--	160/7	164/15	mA
		Others	--	153/7	157/15	mA
	24VDC Input	3.3VDC Output	--	67/5	69/15	mA
		5/9VDC Output	--	99/5	101/15	mA
Others		--	95/5	98/15	mA	
Reflected Ripple Current			--	15	--	mA
Surge Voltage	5VDC Input		-0.7	--	9	VDC
	12VDC Input		-0.7	--	18	VDC
	15VDC Input		-0.7	--	21	VDC
	24VDC Input		-0.7	--	30	VDC
Input Filter Type			Capacitance Filter			
Hot Plug			Unavailable			

Output Specifications

Item	Operating Conditions		Min.	Typ.	Max.	Unit
Output Voltage Accuracy			See below: Error Envelope Curve			
Line Regulation	Input Voltage Variation $\pm 1\%$	3.3VDC Output	--	± 1.5	--	%
		Others	--	± 1.2	--	%
Load Regulation	10%~100% Load	3.3VDC Output	--	12	--	%
		5VDC Output	--	12	--	%
		9VDC Output	--	8	--	%
		12VDC Output	--	8	--	%
		15VDC Output	--	8	--	%
		24VDC Output	--	6	--	%
Ripple & Noise	20MHz Bandwidth (Peak-to-Peak)		--	45	120	mVp-p
Temperature Coefficient	Full Load		--	± 0.02	--	%/°C
Short Circuit Protection			Continuous, Self-Recovery			

Note: Ripple & noise are measured at 20MHz of bandwidth with a 10uF electrolytic capacitor and a 1uF ceramic capacitor connected in parallel at the output.

General Specifications

Item	Operating Conditions	Min.	Typ.	Max.	Unit
Isolation Voltage	Input-output, test time 1 minute, leakage current less than 1mA	3000	--	--	VDC
Insulation Resistance	Input-output, insulated voltage 500VDC	1000	--	--	MΩ
Isolation Capacitance	Input-Output,100KHz/0.1V	--	20	--	pF
Operating Temperature	Derating when operating temperature $\geq 85^{\circ}\text{C}$ (See Temperature derating curve chart)	-40	--	105	$^{\circ}\text{C}$
Storage Temperature		-55	--	125	$^{\circ}\text{C}$
Case Temperature Rise	Ta=25 $^{\circ}\text{C}$, Nominal Input, Full Load	--	25	--	$^{\circ}\text{C}$
Storage Humidity	Non-condensing	5	--	95	%RH
Reflow Soldering Temperature	Peak temperature Tc $\leq 245^{\circ}\text{C}$, maximum time above 217 $^{\circ}\text{C}$ is 60 seconds				
Switching Frequency	Full load, nominal input voltage	--	220	--	kHz
MTBF	MIL-HDBK-217F@25 $^{\circ}\text{C}$	>3500Kh			

Mechanical Specifications

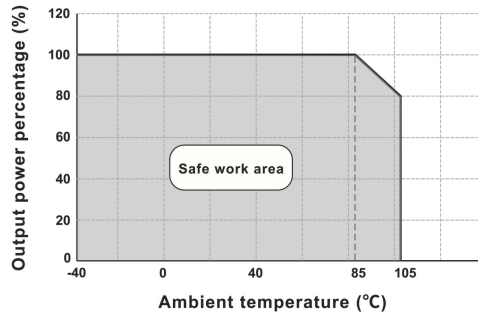
Case Material	Black flame-retardant and heat-resistant plastic (UL94V-0)
Mechanical Dimensions	15.24 * 11.40 * 7.25mm
Weight	1.7g(typ.)
Cooling Method	Free air convection

Electromagnetic Compatibility (EMC)

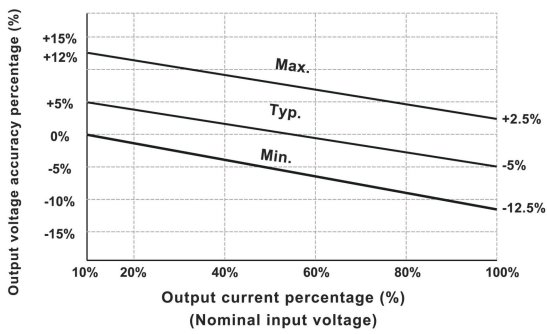
EMI	CE	CISPR32/EN55032 CLASS B (EMC recommended circuit see Fig. 2)
	RE	CISPR32/EN55032 CLASS B (EMC recommended circuit see Fig. 2)
EMS	ESD	IEC/EN61000-4-2 Contact $\pm 6\text{KV}$, Air $\pm 8\text{KV}$ perf. Criteria B

Product Characteristic Curve

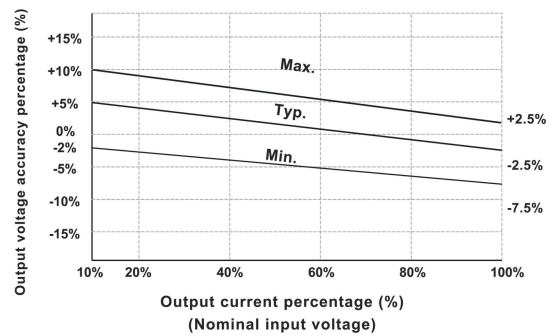
Temperature Derating Curve



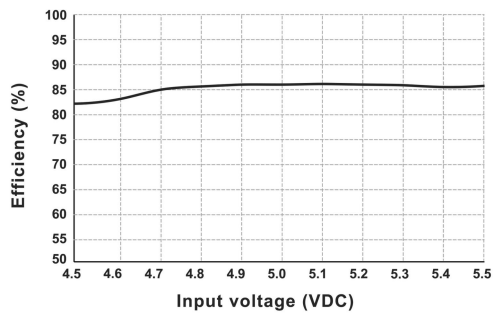
Output Regulation Curve (3.3V Output)



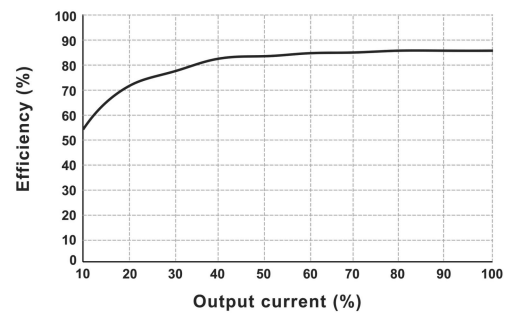
Output Regulation Curve (Others)



Efficiency vs Input Voltage (Full Load, DF2-05S05LT)

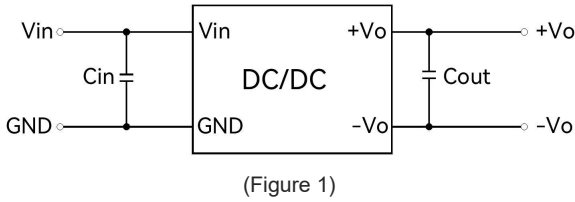


Efficiency vs Output Load (Vin=5V, DF2-05S05LT)



Design Reference - Application Circuit

Application circuit



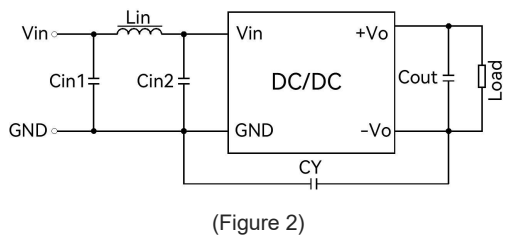
Recommended Capacitive Load Value Table

Vin	Cin	Vo	Cout
5VDC	4.7uF/16V	3.3/5VDC	10μF/16V
9/12VDC	2.2uF/25V	9/12VDC	4.7μF/25V
15VDC	1.0uF/25V	15VDC	1.0μF/25V
24VDC	1.0uF/50V	24VDC	0.47μF/50V

All DC/DC converters in this series are tested according to the recommended application circuit (Figure 1) before the shipment. If further reduction of input and output ripple is required, the external capacitors C_{in} and C_{out} at the input and output terminal can be increased in value, or capacitors with low equivalent series impedance can be selected. For each output, under safe and reliable operating conditions, the maximum capacitance of the filter capacitor must not exceed the maximum capacitive load of the product.

Design Reference - EMC Solutions - Recommended Circuits

EMC Recommended Circuit Design and Application



Recommended Parameter Table

Cin1	4.7μF /50V
Cin2	4.7μF /50V
Cout	Refer to Cout parameters in Figure 1
CY	1000pF/4kVDC
Lin	6.8μH

Note:

1. Typical Application

If further reduction of input and output ripple is required, a Capacitance Filter network can be connected at the input and output terminals, as shown in the application circuit in Figure 1. However, appropriate filter capacitors should be selected. If the capacitor is too large, it may cause startup issues. For each output, under conditions ensuring safe and reliable operation, the recommended capacitive load values are detailed in the table.

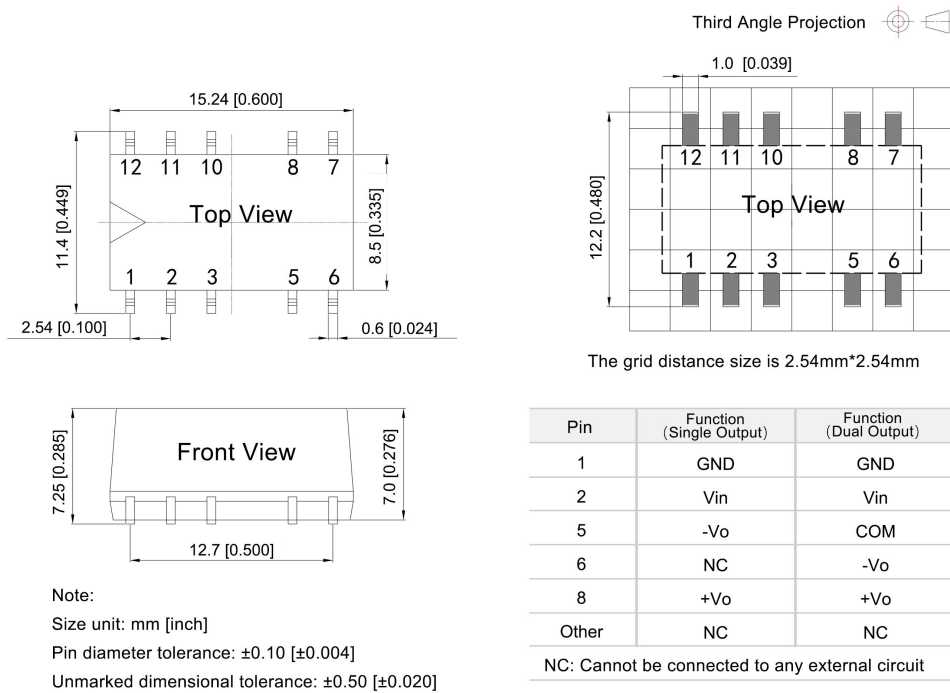
2. EMC Recommended Circuit: See Figure 2

3. Output Load Requirements

To ensure efficient and reliable operation of the module, the minimum output load during use must not be less than 10% of the rated load. If the required power is indeed small, please connect a resistor in parallel at the output (the sum of the resistor's power dissipation and the actual usage power should be greater than or equal to 10% of the rated power).

Dimensions and Recommended Layout

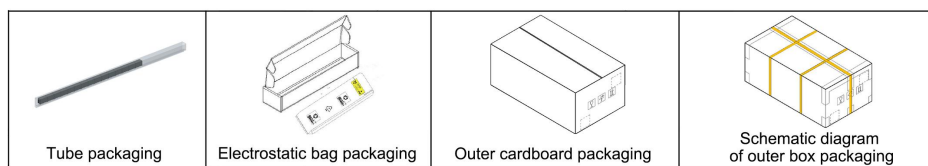
DF2-xxSxxLT Dimensions and Recommended Layout



Packaging Information

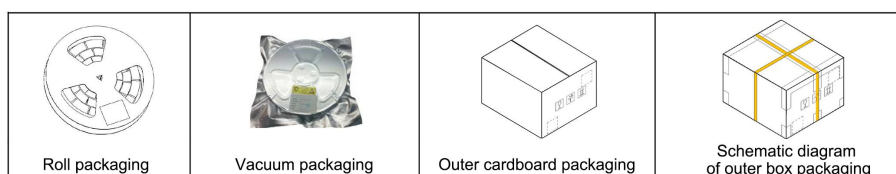
Model series (Tube packaging)	Quantity per tube (pcs/ tube)	Quantity of electrostatic bag (pcs/ bag)	Quantity of inner box (pcs/ box)	Full box Quantity (pcs)
DF2-xxSxxLT	38	760	2280	9120

The schematic diagram of tube packaging is shown below:

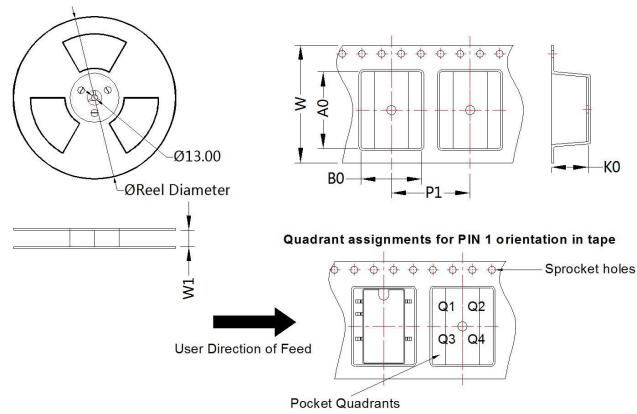


Model series (Reel packaging)	Quantity of products carried (pcs/ROLL)	Quantity of electrostatic bag (pcs/ bag)	Quantity of inner box (pcs/ box)	Full box Quantity (pcs)
DF2-xxSxxLT	500	500	1500	3000

The schematic diagram of Reel packaging is shown below:



Package type	Pin	MPQ	Reel Diameter (mm)	Reel Width W1(mm)	A0(mm)	B0(mm)	K0(mm)	P1(mm)	W(mm)	Pin1 Quadrant
SMD	10	500	330	24.5	15.6	12.4	7.45	16	24	Q1



Product precautions

1. The input voltage should not exceed the specified range value, otherwise it may cause permanent and irreparable damage;
2. It is recommended to use at a load of over 5%. If the load is below 5%, the ripple index of the product may exceed the specifications, but it does not affect the reliability of the product;
3. The maximum capacitive load is tested within the input voltage range and under full load conditions;
4. Unless otherwise specified, all indicators in this manual are measured at $T_a=25\text{ }^\circ\text{C}$, humidity<75% RH, nominal input voltage, and output rated load;
5. All indicator testing methods in this manual are based on our company's corporate standards;
6. Our company can provide product customization, and specific requirements can be directly contacted by our technical personnel;
7. Product specifications are subject to change without prior notice.

Manufacturer contact information

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